

Page 52, line 21, delete "R" and insert -- autocorrelation --.

Page 53, line 12, delete "R" and insert -- autocorrelation --.

Page 54, line 18, delete "equation".

AS
Page 56, line 26, after "65536." insert -- Knuth, D. "The Art of Computer Programming, Volume 2, Seminumerical Algorithms," Addison-Wesley, 1969. --.

Page 60, line 20, delete " $e(-j2\pi f_{mid})$ " and insert -- $e^{(-j2\pi f_{mid})}$ --.

Page 63, line 9, delete "progress tone" and insert -- Progress Tone --.

VW
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19
Page 68, line 31-page 69, line 7, delete "In one embodiment, the resource manager can be implemented to reduce complexity when the worst case system loading exceeds the peak system resources. The worst case system loading is simply the sum of the worst case (peak) loading of each service invoked by the network VHD and its associated PXDs. However, the statistical nature of the processor resources required to process voice band telephony signals is such that it is extremely unlikely that the worst case processor loading for each PXD and/or service will occur simultaneously. Thus, a more robust (lower overall power consumption and higher densities, i.e. more channels per DSP) signal processing system may be realized if the average complexity of the various voice mode PXDs and associated services is minimized. Therefore, in the described exemplary embodiment, average system complexity is reduced and system resources may be over subscribed (peak loading exceeds peak system resources) in the short term wherein complexity reductions are invoked to reduce the peak loading placed on the system." and insert -- The resource manager can be implemented to reduce complexity when the worst case system loading exceeds the peak system resources. The worst case system loading is simply the sum of the worst case (peak) loading of each service invoked by the network VHD and its associated PXDs.

AB
The statistical nature of the processor resources required to process voice band telephony signals is such that it is extremely unlikely that the worst case processor loading